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Asymptotic Analysis And Design Of Iterative Receivers For Non Linear ISI Channels

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Abstract—In this paper, iterative receiver analysis and design for non linear satellite channels is investigated. To do so, an EXtrinsic Information Transfer (EXIT) chart-based optimization is applied using two major assumptions: the equalizer outputs follow a Gaussian Mixture distribution since we use non-binary modulations and partial interleavers are used between the Low Density Parity Check (LDPC) code and the mapper. Achievable rates, performance and thresholds of the optimized receiver are analysed. The objective in fine is to answer the question: Is it worth optimizing an iterative receiver for non linear satellite channels?

Keywords—iterative equalization, Volterra series, LDPC code design, optimization

I. INTRODUCTION

The advent of turbo-codes [1], and the reinvention of Gallager codes (LDPC) enabled reaching near Shannon limit performance. Hence, designing capacity approaching codes has gained a wide attention for both memoryless and memory channels in the last decade. This paper addresses the issue of designing LDPC codes for non linear memory channels, and more specifically non linear Volterra channels. In fact, satellite communications are subject to non linear Inter Symbol Interference (ISI). This interference results from the use of power amplifiers near to their saturation region for a better energy efficiency. Thus, input satellite signals with high dynamics -either because of the transmit filters or because of non constant amplitude constellations- are non linearly amplified and hence distorted. This phenomenon is a limiting issue for satellite standards such as DVB-S2 [2] which proposed non-constant amplitude modulations such as 16/32 Amplitude and Phase Shift Keying (APSK). The non linear Volterra channel can be seen as a memory channel represented by a state machine, or a trellis, with non linear branch metrics. This representation allows applying optimal symbol and sequence detection techniques. Yet, joint equalization and decoding following the principle of turbo-equalization proposed in [3] can dramatically enhance system performance. A further enhancement can be achieved when the channel code is designed to best fit the equalization behaviour. Code design and optimization is however only interesting when the channel characteristics do not vary rapidly which is the case of the satellite non linear channel. A great deal of literature addressed the optimization of LDPC codes for static linear ISI channels [4] and partial response ISI channels [5]. Authors in [6] used curve fitting of the EXIT charts to design the code, by considering a serial concatenation of a block containing the detector and the variable nodes with a block of check nodes. Application to the Multiple Input Multiple Output (MIMO) over fading channels with Channel State Information (CSI) at the receiver was investigated. Another optimization approach is presented in [7] and uses a factor graph description for both the detector and the decoder. A simple derivation of the Sum Product (SP) of the overall receiver factor graph is then presented. As far as non linear satellite channels are concerned, authors in [8] optimized the non-iterative 16-APSK constellation ring ratios in order to allow for the best performance for different LDPC coding rates and spectral efficiencies. Authors in [9] investigated the performance of MAP turbo-equalization for non linear satellite channels. Yet, no thorough iterative receiver optimization for non linear channels has yet been investigated. Hence, we are interested in the analysis and the code design for turbo MAP equalization over non linear ISI satellite channels. A serial concatenation approach similar to [6] is used, but the equalizer outputs are assumed to follow a Gaussian Mixture (GM) distribution since the Gaussian approximation is not accurate for non-binary modulations. In addition, partial interleavers will be used for variable nodes of the same degree in order to allow for an independence assumption among nodes of the same degree. The rest of this paper is organised as follows: Section II presents the Volterra non linear channel with trellis representation. Section III outlines the MAP turbo equalizer where Section IV presents the concepts of EXIT charts applied to 16-APSK and LDPC variable and check nodes. Section V presents the optimization problem as well as obtained results. Section VI draws conclusions and perspectives.

II. NONLINEAR VOLterra CHANNEL

Let us consider the scheme depicted in Fig. 1. A source produces blocks of $K$ independent and identically distributed bits $b = (b_0, \ldots, b_{K-1}) \in GF(2)^K$ which are encoded with a binary LDPC code with code rate $R = K/N$ producing blocks of codewords $c = (c_0, \ldots, c_{N-1}) \in GF(2)^N$ of length $N$. A binary LDPC code is defined by its $(N - K, N)$ parity check matrix which allows for a representation of the LDPC code using a Tanner graph involving two types of vertices,
namely Variable Nodes (VN) and Check Nodes (CN). A codeword \( c \) satisfies \( Hc^T = 0 \). A variable node \( m \) (on the \( m \)th column of \( H \)) is related to a check node \( n \) (\( n \)th row of \( H \)) by edge \( E_{n,m} \) when \( H_{n,m} = 1 \). By defining \( \lambda_i \) (resp. \( \rho_j \)) as the proportion of edges related to degree-\( i \) VNs (resp. to degree-\( j \) CNs), an LDPC code ensemble can also be characterized by the distribution polynomials of edges degree writing as follows:

\[
\lambda(X) = \sum_{i=2}^{d_v} \lambda_i X^{i-1} \quad \rho(X) = \sum_{j=2}^{d_c} \rho_j X^{j-1}
\]

(1)

where \( d_v \) (resp. \( d_c \)) represents the maximum degree of variable (resp. check) nodes. Codewords are then fed to a mapping function with cardinality \( M \) which converts blocks \( x_n \in X \) where \( X \) is the mapping alphabet. A partial interleaver is used to interleave the incoming codewords belonging to variable nodes of the same degree (justification and details can be found in Section III). The symbols \( x_n \) are then passed through the non linear satellite channel.

The satellite power amplifier is a memoryless device that has an amplification characteristic which is only function of the amplitude of the signal to be amplified. However, due to satellite input and output filters, the satellite transponder is equivalent to a memory channel. At the receiver a Gaussian circular noise \( w_n \) corrupts the input signal. It has been shown in [10] that the received symbols can be written as a mixture of linear and non linear ISI terms, namely:

\[
z_n = \sum_{m=0}^{v_m} \sum_{n_1=-\infty}^{\infty} \cdots \sum_{n_{2m+1}=-\infty}^{\infty} x_{n-n_1} \cdots x_{n-n_{2m+1}} h_{\lambda_1 \ldots \lambda_m} + w_n
\]

(2)

where \( v_m \) defines the decomposition order of the Volterra series, \( h_{\lambda_1 \ldots \lambda_m} \) are Volterra kernels and \( w_n \sim \mathcal{N}(0, \sigma_w^2) \) is the filtered sampled additive noise. In fact, the Volterra kernels are not only function of the chain filters but also on the mapping alphabet \( X \). As argued in [10], non linear kernels with order greater than five have negligible contribution to the ISI terms, which yields a third order decomposition as follows:

\[
z_n = \sum_{i \in \mathcal{I}_i^p} h_i x_{n-i} + \sum_{i \in \mathcal{I}_i^p, j \in \mathcal{I}_j^p} \sum_{k \in \mathcal{I}_k^p} h_{ijk} x_{n-i} x_{n-j} x_{n-k} + w_n
\]

\[
= \mathcal{F}((x_{n-k})_{k \in \mathcal{I}}, x_n) + w_n
\]

where \( \mathcal{I}_i^p \) denotes the index set of symbols at position \( p \) in the \( i \)th order of the Volterra decomposition in (2) and \( \mathcal{I} = \cup_{i \in I} \{ \mathcal{I}_i^p \} \setminus \{ \emptyset \} \). We assume that the Volterra decomposition is causal i.e. \( \forall i \in \mathcal{I}_i^p, j \geq 0 \)

III. NON LINEAR CHANNEL ITERATIVE RECEIVER

A. Iterative MAP equalizer

The non linear Volterra channel as described by (3) can be represented by trellis with a state set \( S \) with cardinality \( |S| = M_{\max}+1 \) and \( M \) transitions per state, where \( l_{\max} = 3 \). The branch transitions outputs are both linear and non linear functions of the memory symbols following (3). This representation allows for an optimal symbol detection based on MAP equalization. More specifically, we use the Bahl-Cocke-Jelinek-Raviv (BCJR) efficient MAP implementation. The LDPC code concatenated with a Markov process channel allows for using joint iterative equalization and decoding which yields better error rates, yet, with a significant complexity increase. More specifically, let \( L_{o}(c_{o,i}'), L(c_{o,i}'), L_{a}(c_{a,i}) \) and \( L_{e}(c_{e,i}) \) be the a priori, posteriori and extrinsic Log Likelihood Ratios (LLRs) of the \( i \)th coded bit belonging to the \( n \)th symbol \((c_{o,i})\). The a posteriori LLR of coded bit \( (c_{o,i}) \) knowing the whole received symbols sequence \( z = (z_n)_{n \in \mathbb{N}} \) writes as follows:

\[
L_{e}(c_{o,i}) = \log \frac{P(c_{o,i} = 0 \mid z)}{P(c_{o,i} = 1 \mid z)} = \log \frac{\sum_{s_j, s_i=0} P(x_n = s_j, z)}{\sum_{s_j, s_i=1} P(x_n = s_j, z)}
\]

(4)

where for \( j = 1, \ldots, M \), \( P(x_n = s_j, z) \) is computed following the forward/backward recursion.

B. Iterative LDPC decoder and scheduling

We will assume that Belief Propagation (BP)-based LDPC decoding is used [11]. Besides, MAP equalization is assumed to be run independently on each group delimited by a partial interleaver i.e. transition effects are neglected. In this paper, the following scheduling will be used: a global iteration \( t \) is composed of one BCJR forward-backward recursion for the
equalizer followed by one BP iteration (one data-pass plus check-pass update) for the LDPC code. We further assume partial interleaving $\Pi_{\mu}$ between the channel equalizer and the LDPC code operated degree-wise, i.e. each partial interleaver is associated with the VNs set of the same degree. The assumption is similar to [6] and ensures an efficient optimization problem statement which becomes linear. Furthermore, and for independence considerations, a global interleaver $\Pi_{LDPC}$ is assumed between check and variable nodes as depicted in Fig. 2 representing the classical code ensemble interleaving.\(^1\)

IV. ASYMPTOTIC CODE DESIGN USING EXIT CHARTS

Extrinsic Information Transfer chart is a common tool to analyse the convergence behaviour of a Soft Input Soft Output (SISO) component. For a binary input AWGN channel $Y = aX + N$, the output extrinsic LLRs follow a consistent Gaussian distribution $N(\mu, 2\mu)$ where $\mu = 2\sigma^2$ and $\sigma^2$ is the noise variance. The mutual information of LLRs following Gaussian consistent distribution writes as:

$$J(\sigma) = 1 - E_x \left( \log_2(1 + e^{-x}) \right)$$

(5)

where expectation is taken with respect to $x \sim N(\frac{\sigma^2}{2}, \sigma^2)$. For Gray mapped QPSK, it can be shown that the soft-demapper extrinsic output LLR is a mixture of two consistent Gaussian distributions with mean and variance independent of the input a priori. As far as higher order gray-mapped modulations are concerned, the output extrinsic LLRs are Gaussian mixtures with parameters depending on the input a priori information. However, the slight impact of the a priori on the parameters of the GM distribution for medium to large Signal to Noise Ratios (SNRs), allows for a flat EXIT chart approximation at a given SNR. This approximation also holds for the 16-APSK which has a quasi gray mapping. The mutual information of LLRs following a GM distribution $\sum_{m=1}^{M_G} \pi_m N(\frac{\sigma^2}{2}, \beta_m \sigma^2)$, where $M_G$ is the number of mixtures, writes as follows:

$$\Psi(\sigma) = \sum_{m=1}^{M_G} \pi_m J(\sqrt{\beta_m} \sigma)$$

(6)

In order to optimize the LDPC code for a given receiver structure, mutual informations at the input and output of the equalizer, variable and check nodes are evaluated. The objective is to optimize the edge-degree distribution polynomials $\lambda(X)$ and $\rho(X)$ in order to achieve the highest rate under some set of constraints.

A. MAP equalizer EXIT chart

Let $I_{MAP, V}$ denote the extrinsic output mutual information of the MAP equalizer in response to an input information $I_{V, MAP}$ coming from a variable node. The EXIT curve plots for a given SNR, $I_{MAP, V} = T(I_{V, MAP})$, where $T$ is a non-decreasing function. Computations of the MAP EXIT curves are analytically complex, but numerical simulations can provide good approximations of the real curves. These EXIT charts are also exploited in finding an upper bound to the achievable rate of the iterative receiver. The upper bound, is given by the so called area theorem [12] which states that for serially concatenated codes and Binary Erasure Channel (BEC), the achievable rate $R$ of an outer code, is upper-bounded by the integral of the inner code transfer curve $T$ i.e. $R \leq \int_0^1 T(I) dI$ when the latter is a rate 1 code. It is widely observed that this upper bound is a good approximation for other channel models. At perfect a priori information, and for other channel models. At perfect a priori information, the performance of the MAP joins that of the soft demapper for a 16-APSK Bit Interleaved Coded Modulation (BICM). Thus, the GM distribution in (6) is a good approximation of the mutual information of the MAP equalizer output LLRs. The mutual information of 16-APSK at the output of the AWGN soft demapper is computed for each SNR at input information $I_A = 1$. The parameters of the Gaussian mixture $(\pi_m, \beta_m)$ were estimated using a Maximum-Likelihood with Expectation Maximisation (EM) algorithm and are presented in table I. The inverse of function $\Psi$ is computed using trust-region-reflective non linear least square approximation, after a coarse estimation of the turning point. $\Psi^{-1}$ writes as follows:

$$\Psi^{-1}(I) = \begin{cases} 
\alpha_1{I^4} + \alpha_2{I^3} + \alpha_3{I^2} + \alpha_4 {I} & \text{if } I < 0.5654 \\
-b_1 \log(-b_2(I-1)) + b_3 & \text{if } I \geq 0.5654 
\end{cases}$$

(7)

where $(b_1, b_2, b_3) = (1.2570, 0.9284, 3.1477)$ and $(\alpha_1, \ldots, \alpha_4) = (-99.4002, 130.8198, -58.3329, 14.3040)$.

Figure 3 plots the mutual information of 16-APSK mapped symbols for different approximations. The estimated Gaussian

\(^1\)Note that the proposed approach is not a Multi-Edge Type (MET) due to the uniform interleaving of edges of the LDPC code.
mixture leads to a fine approximation of the measured mutual information at the output of the demapper.

B. LDPC check and variable nodes EXIT charts

For ease of presentation, the equalizer output LLRs will be noted as $L_{MAP}^i$, where the notation $L_{X,Y}^i$ designates the LLR sent from $X$ to $Y$ at iteration $t$.

At iteration $t$:

1) A variable node $V$ of degree $i$ receives $(i + 1)$ LLR messages namely $i$ check nodes LLRs $L_{MAP}^{i-1}$ following a Gaussian distribution, and the equalizer extrinsic LLR $L_{MAP}^i$ following a GM distribution. It computes then the output LLR $L_{VC}^i$ on the branch $n$:

$$L_{VC}^i(n) = L_{MAP}^i + \sum_{k\neq n, k=1}^i L_{CkV}^{i-1}$$

(8)

The resulting LLR follow a GM distribution which is the convolution of a Gaussian and a GM distribution. More specifically,

$$I_{VC}^i(i) = \sum_{m=1}^{M_G} \pi_m J \left( \sqrt{\beta_m \Psi^{-1} \left( I_{MAPV}^{i-1} \right)^2 + (i-1)J^{-1} \left( I_{CkV}^{i-1} \right)^2} \right)$$

(9)

where $I_{MAPV}^{i-1} = T \left( I_{VC}^{i-1} \right)$. Thus, the overall variable nodes output mutual information $I_{VC}^i$ is expressed as follows:

$$I_{VC}^i = \sum_{i=2}^{d_v} \lambda_i I_{VC}^i(i)$$

(10)

It is worth noting that by taking $M_G = 1$, $\beta_m = 1$ and $\pi_m = 1$, equation (9) simplifies to the Gaussian approximation in [6].

2) A check node $C$ of degree $j$ receives $j$ variable nodes LLRs. The output LLR at an edge $m$ is computed by a non linear combination of the input messages as follows:

$$L_{CV}^i(m) = 2 \tanh^{-1} \left( \prod_{k \neq m} \tanh \left( \frac{L_{VC}^i(m)}{2} \right) \right)$$

(11)

The characterisation of the pdf of the output check node LLR is not straightforward. However, by using a consistent Gaussian approximation, one can compute the mean of the output LLR and project it on the right mutual information [13]. However, a simpler yet efficient approximation named ‘reciprocal channel’ can be used [6]. Thus, the mutual information of the check node is expressed as:

$$I_{CV}^i = 1 - \sum_{j=2}^{d_c} \rho_j J \left( \sqrt{j-1} J^{-1} \left( 1 - I_{VC}^i \right) \right)$$

(12)

3) A variable node $V$ of degree $i$ computes the extrinsic LLR to be forwarded to the equalizer as apriori information $L_{V,MAP}^i$ as follows:

$$L_{V,MAP}^i(i) = \sum_{k=1}^i L_{CkV}^i$$

(13)

Thus, the associated mutual information for a variable node of degree $i$ writes as:

$$I_{V,MAP}^i = J \left( \sqrt{2} J^{-1} \left( I_{CkV}^i \right) \right)$$

(14)

V. OPTIMIZATION AND CODE DESIGN

Combining equations (9), and (10), and (12), and (14), the variable to check node information at iteration $t$, $I_{VC}^i$, writes in a parametric recursive way as:

$$I_{V,MAP}^i = G \left( \lambda(X), I_{V,MAP}^i, \rho(X), T(1) \right)$$

(15)

This function is non linear in the mutual information $I_{VC}^i$, $\rho(X)$ but linear in the variable node-degree distribution polynomial $\lambda(X)$ for fixed values of the aforementioned parameters. Thus, by fixing a distribution $\rho(X)$, which is generally considered with concentrated degrees, optimization consists of maximizing $\sum_i \lambda_i$ subject to the following constraints:

$$\begin{cases}
\text{Convergence} & G(\lambda, x, \rho, T(\cdot)) \geq x \\
\text{Proportions} & \sum_j \lambda_j = 1 \\
\text{Stability} & \lambda_2 \sum_{m=0}^{M_G} \pi_m \beta_m \Psi^{-1}(T(1))/8 \\
& < \frac{1}{\sum_{j=m}^\infty \rho_j (j-1)}
\end{cases}$$

For 16APSK, $T(1) = \Psi(\sqrt{2||h||^2/\pi^2})$ where $||h||^2$ is the channel energy.

A. Optimization example

In this section, we present optimization results obtained for a MAP turbo equalizer over a Volterra channel. The channel coefficients in Tab. II are excerpted from [14] and have been computed using system identification. Fig. 4 plots the achievable rate of both the 16-APSK BICM, computed using the EXIT area approximation of a soft demapper over AWGN channel, and the MAP equalizer computed also using the area integral. It also plots the optimized rates, using both the Gaussian [6] and the GM mixture approximations previously derived. The iterative receiver, achieves a gain of 1dB over the non iterative MAP equalization for the non linear channel.

<table>
<thead>
<tr>
<th>Test channel Volterra kernels</th>
</tr>
</thead>
<tbody>
<tr>
<td>$h_0^{(1)} = 0.0074 - j0.0046$</td>
</tr>
<tr>
<td>$h_1^{(1)} = -0.0429 + j0.0151$</td>
</tr>
<tr>
<td>$h_0^{(2)} = 0.1603 - j0.0634$</td>
</tr>
<tr>
<td>$h_1^{(2)} = 0.3415 - j0.1226$</td>
</tr>
<tr>
<td>$h_0^{(3)} = -0.50 - j0.0006$</td>
</tr>
<tr>
<td>$h_1^{(3)} = 0.0148 - j0.0019$</td>
</tr>
<tr>
<td>$h_0^{(4)} = -0.0237 + j0.0008$</td>
</tr>
<tr>
<td>$h_1^{(4)} = -0.0087 + j0.0006$</td>
</tr>
<tr>
<td>$h_0^{(5)} = -0.0108 + j0.0010$</td>
</tr>
</tbody>
</table>
following the optimal degree-distribution $\lambda$ and $\rho$, should start to fall around $3dB$. Fig. 6 plots the Bit Error Rate of a 16-APSK turbo equalized with an outer LDPC code generated following the optimal degree distribution and an AWGN optimized code. Approximately $0.3dB$ is gained optimizing the code. The estimated threshold is around $E_b/N_0 = 3.2$.

VI. CONCLUSIONS

An iterative receiver for a non linear satellite channel was analysed and designed using a Gaussian mixture approximation of the equalizer output and partial interleaving of variable nodes. The results prove that the designed receiver achieves better performance than the non iterative optimal receiver. Besides, the study could be extended to other receiver designs namely density evolution-based, protograph codes and MET based code design.

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